

Jerald Yoo

Short CV – Nov. 2017

Associate Professor, National University of Singapore (NUS)

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Education

- Ph.D. in Electrical Engineering and Computer Science (EECS), Korea Advanced Institute of Science and Technology (KAIST), Daejeon, Korea, 2010
- M.S. in EECS, KAIST, Daejeon, Korea, 2007
- B.S. in EECS, KAIST, Daejeon, Korea, 2002

Work Experience

- Associate Professor, National Univ. of Singapore (Jan 2017~)
- Distinguished Lecturer, IEEE Solid-State Circuits Society (SSCS) (Jan. 2017~)
- Associate Professor, Masdar Institute of Science and Technology, UAE (Jun. 2010-Dec.2016)
- Vice Chair, IEEE Solid-State Circuits Society (SSCS) UAE Chapter (Jul. 2015-Dec. 2016)
- Visiting Scholar, Massachusetts Institute of Technology, MA, USA (June 2010-June 2011)
- Assistant Manager, TritonTech, Inc., Seoul, Korea (Jan. 2002-Nov. 2004)

Research Interests

- Circuits and systems for wearable healthcare / biomedical applications
- Body Area Network (BAN) using human Body Coupled Communication (BCC)
- ASIC for mobile ultrasound applications
- Planar-Fashionable Circuit Board (P-FCB): textile platform for biomedical sensors

Qualification

- Pioneered the research in BAN/BCC/Wearable Healthcare and P-FCB
- 7 ISSCC Papers, 15 IEEE Journals (9 IEEE JSSC) (authored/ co-authored)
- ISSCC Short Course (2014), Evening Session (2014/2013), Evening Panel (2012) and Forum (2011) talks / Symp. VLSI Circuits Evening Session (2017) panel
- Invited talks at Cadence (2016), Qualcomm (2015), Maxim Integrated (2015), Texas Instruments (2012) and Intel (2010)
- 3 US patents / 6 Korean patents (issued), 6 US/PCT pending
- 12 research grants (worth 2.66 million US\$) since June 2010
- Technical Program Committee Member of IEEE CICC/A-SSCC/ISSCC SRP/CASS ASPTC
- www.jeraldyyoo.com / <https://scholar.google.com/citations?user=NHaSuGgAAAAJ&hl=en>

Teaching

- NUS, CEG2027 Introduction to Digital Circuits (developed, will be offered from Fall 2018),
- NUS, EE3407 Analog Electronics (co-teaching with Prof. Aaron Danner)
- NUS, EE2020 Digital Fundamentals
- Masdar Institute, MIC610 Analysis and Design of Digital Integrated Circuits (developed)
- Masdar Institute, MIC614 Low Energy Biomedical Circuits and Systems (developed)
- Masdar Institute, MIC615 Computer Architecture (co-developed with Prof. Ibrahim Elfadel)
- Mass. Institute of Technology (MIT), 6.374 Digital Integrated Circuits (A guest lecture Fall 2010)

Honors

- Best Research Award (Masdar Institute), May 2015.
- Best Paper Award, IEEE Int. Symp. Circ. Sys (ISCAS) 2015, BioCAS Track, May 2015.
- Outstanding KAIST Electrical Engineering Research Award, 2007, 2008 and 2009.
- A-SSCC Outstanding Student Design Contest (Co-recipient), Nov. 2006.